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CENTRAL FAX CENTER****APR 19 2006****IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Visokay et al. Docket No.: TI-35227  
Serial No.: 10/734,708 Art Unit: 2813  
Filing Date: 12/11/2003 Examiner: Thanhha S. Pham  
Customer No.: 23494 Conf. No.: 2373  
Title: Method for Fabricating Transistor Gate Structures and Gate Dielectrics  
Thereof

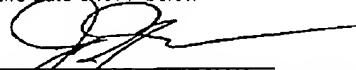
**REQUEST FOR EXTENSION OF TIME**

Commissioner of Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

**CERTIFICATION OF FACSIMILE TRANSMISSION**

I hereby certify that the following papers are being transmitted by  
facsimile to the U.S. Patent and Trademark Office at 571-273-8300 on  
the date shown below:

  
Jacqueline J. Garner, Reg. No. 36,144

April 19, 2006  
Date

Pursuant to 37 CFR 1.136(a), Applicant(s) respectfully petitions the Commissioner  
for an extension of the shortened statutory period for response in the above-identified  
Application.

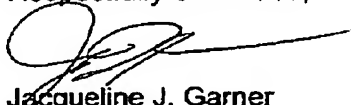
The fee for this extension is indicated below:

- ☐ One Month (\$120)  
☐ Two Months (\$450)  
☒ Three Months (\$1,020)  
☐ Four Months (\$1,590)

Please charge the fee to deposit account no. 20-0668. Any further necessary  
extension of time is hereby requested. Charge any and all fees to deposit account no. 20-  
0668.

Texas Instruments Incorporated  
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Respectfully Submitted,

  
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04/20/2006 BABRAHA1 00000054 200668 10734708

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